

Power Exchange by Using Micro-grid Inverter with High-Voltage Gain for Photovoltaic Applications

M. R. Banaei and E. Salary

Abstract—In this paper, power exchange in a clean energy system using multi-stage converter based on combination of novel dc/dc converter and the nine-switch inverter is presented. Power exchange between grid and renewable energies is one advantage of smart grid. The nine-switch converter consists of a one capacitor link and nine semiconductor switches and it has two output ports. The proposed dc/dc converter has high gain. The presented systems connect photovoltaic energy sources to load and grid. In proposed structure photovoltaic source inject energy to load and grid. If the photovoltaic source can't feed the load, the grid can feed load through nine-switch converter. The simulation results by MATLAB/SIMULINK show ability of presented systems in power generation.

Index Terms—Photovoltaic, dc/dc converter, nine-switch converter, power exchange.

I. INTRODUCTION

Nowadays, the use of renewable energy and green sources is gaining attention. Photovoltaic (PV) systems and fuel cell (FC) are new source of energy that generates dc voltage [1], [2]. The dc/ac inverters are power electronic devices used to produce mains voltage ac power from voltage dc sources (battery, PV or FC)[3]. For the energy conversion, the typical converter is based on a multi-stage converter and specially two-stage converter. Two-stage configuration is mainly used because of its advantages of easy control since maximum power point tracking (MPPT) control and current injection control [3], [4]. For example in the PV system the first stage is a dc/dc boost type converter responsible for both extracting the MPPT and boosting the PV voltage to a value higher than the peak of the grid voltage. The second stage is a dc/ac inverter that generates a current to be either injected into the grid or to feed loads.

The last few years, however, have witnessed the introduction of novel technologies and concepts [5]. Power exchange between grid and renewable energies is one advantage of smart grid.

In this paper, a multi-stage micro-grid inverter topology is proposed based on new transformer less high gain dc/dc converter and nine switch inverter. The proposed dc/dc converter has the advantage of high-voltage gain and uses partial power processing technique. In the dc/ac part of multi-stage converter, nine-switch inverter is used [6]-[9]. The structure of the nine-switch inverter is presented in Fig. 1.

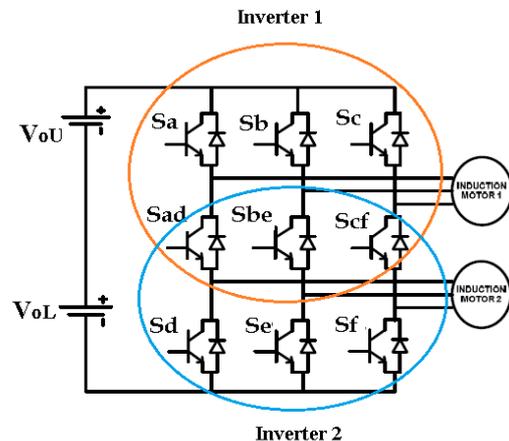


Fig. 1. Nine-switch inverter topology.

This consists of two three-phase inverters combined with three common switches. The Inverter1 consists of switches Sa, Sb, Sc, Sad, Sbe, and Scf and Inverter2 consists of the switches Sad, Sce, Scf, Sd, Se and Sf. The balanced loads are supplied from these inverters. This topology has introduced for feeding two loads, in the first.

II. MULTI-STAGE DC/AC CONVERTER

The proposed multi-stage inverter is shown in Fig. 2. The proposed system consists of novel dc/dc converter cascaded with nine-switch inverter. The dc/dc converter has the advantage of high-voltage gain. The DC voltage generated from a single PV module has small value. These small DC values are not suitable to be used alone to produce the required ac voltage. Increasing the dc/ac conversion gain ratio by increasing the dc/dc conversion gain ratio is one solution to overcome this problem. High efficiency is achieved by having a portion of the input PV power directly fed forward to the output without being processed by the converter. The boost converter switch T_1 is designed to operate at high frequency to decrease the inductor values and the overall system size.

A. dc/dc Converter

Operation of dc/dc converter is the same as conventional boost converter. This converter has two modes of operations. The output voltage is controlled by duty cycle of T_1 switch. T_1 switch has two states and by notice to switching state of T_1 switch, modes of operations are described as two modes. Fig. 3 shows two modes of dc/dc boost converter. Also, to simplify this analysis, the converter will be discussed assuming a simple resistive load R is connected across its terminal instead and the resistance of semiconductors, inductors and capacitors are neglected.

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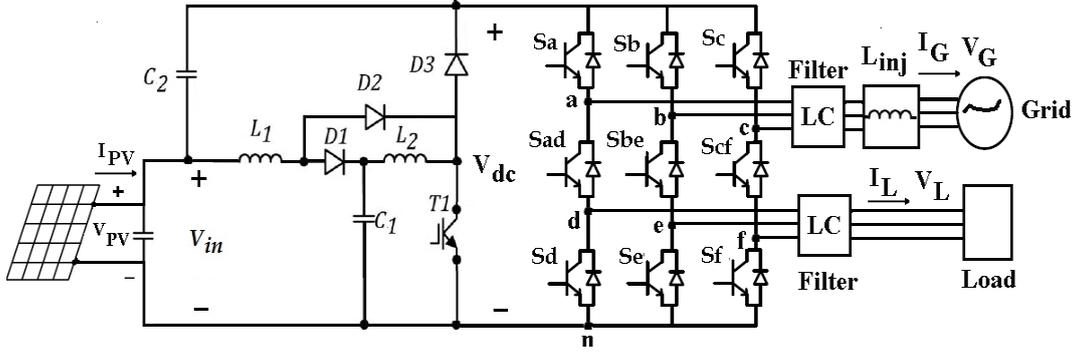


Fig. 2. Proposed multi-stage inverter.

The proposed converter operating in continuous conduction mode (CCM) and discontinuous conduction mode (DCM) but in this paper CCM mode is used because continuous input current is required for using in DG system.

First mode: Occurs when the switch T_1 is ON, this causes diode D_2 to be turned ON and diodes D_1 and D_3 to be turned OFF. Thus the L_2 inductor and C_1 capacitor are parallel and the L_1 inductor and DC voltage source are parallel. In this mode inductors are charged. Fig. 3(a) shows the circuit of mode 1. During mode 1, the voltages across the inductors are

$$V_{L1} = V_{in} \quad (1)$$

$$V_{L2} = V_{C1} \quad (2)$$

The duty cycle is:

$$D = \frac{t_0}{T} = t_0 \cdot f_s \quad (3)$$

where t_0 , T and f_s are the on switch time, period and switching frequency respectively.

Second mode: Occurs when the switch T_1 is OFF, this causes diode D_1 to be turned ON and diodes D_2 and D_3 to be turned OFF. Fig. 3(b) shows the circuit of mode 2.

$$V_{L1} = V_{in} - V_{C1} \quad (4)$$

$$V_{L2} = V_{C1} - V_{dc} \quad (5)$$

By notice the Eq. (1)-(5), the zero average inductors voltages over one carrier cycle are:

$$\begin{aligned} \overline{V_{L1}} &= 0 = DV_{in} + (1-D)(V_{in} - V_{C1}) \Rightarrow \\ V_{in} &= (1-D)V_{C1} \end{aligned} \quad (6)$$

$$\begin{aligned} \overline{V_{L2}} &= 0 = DV_{C1} + (1-D)(V_{C1} - V_{dc}) \Rightarrow \\ V_{C1} &= (1-D)V_{dc} \end{aligned} \quad (7)$$

By replacing Eq. (6) in Eq. (7):

$$V_{dc} = \frac{1}{(1-D)^2} V_{in} \quad (8)$$

The voltage of C_2 is obtained as:

$$V_{C2} = V_{dc} - V_{in} = \frac{1}{(1-D)} V_{in} \quad (9)$$

The PV voltage and current are sensed and fed into the MPPT to generate the dc/dc converter switch (T_1) pulses required to catch the maximum power from the PV. The perturbation and observation (P&O) algorithm with the merit of simplicity is used as a MPPT algorithm [10], [11].

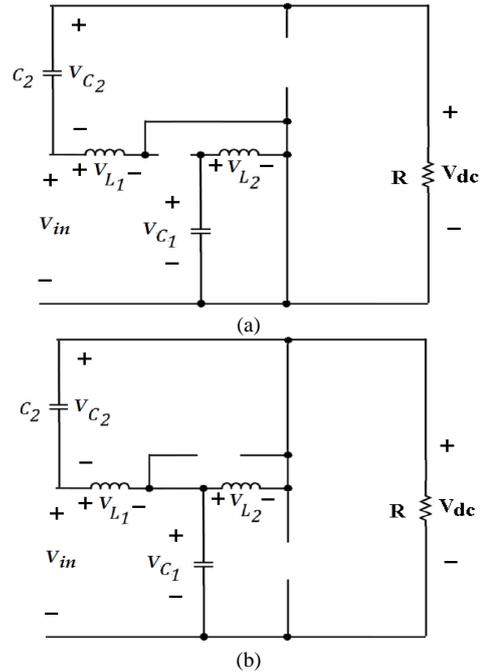


Fig. 3. Modes of DC-DC boost converter (a) mode 1 and (b) mode 2.

B. dc/ac Converter

The nine-switch inverter feed load and inject power to grid. In the proposed system, if PV energy system does not generate necessary energy to feed load, the grid feed load. This is increases power quality of overall system. The output voltages in nine-switch converter can be controlled independently. Switching states of one leg for nine-switch inverter are given in Table I. Fig. 4 shows modulation for one leg. In nine-switch inverter, PWM modulation unit needs six references waveforms [9]. A control technique should be designed for two inverters. One inverter works in grid tied mode and other work in islanding mode. Fig. 5 shows control

block diagram of nine-switch inverter.

$$P_G = P_{PV} - P_L \quad (10)$$

where P_G , P_{PV} and P_L are power of grid, PV and load, respectively. In grid-connected state, voltage of dc-link (V_{dc}) and inject current to the grid are measured and inject current transformed to d - q frame using Park transformation. The dc-link (V_{dc}) voltage is compared with reference value (V_{dc}^*). Resultant value is added to d axis component of inject current to the grid. The result is d component of reference wave. The q axis component of inject current to the grid is compared with reference value ($i_{qG}^*=0$), the error signal is passed through PI controller. The result is q component of reference wave. The generated d and q components transformed again to abc frame by Park inverse-transformation.

TABLE I: CONVERTER OUTPUT VOLTAGES AND SWITCHES STATES

state	ON Switches	Voltage
1	Sd, Sad	$V_{an}=V_{dc}, V_{dn}=V_{dc}$
2	Sad, Sd	$V_{an}=0, V_{dn}=0$
3	Sa, Sd	$V_{an}=V_{dc}, V_{dn}=0$

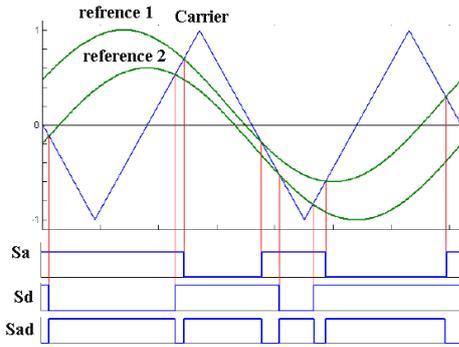


Fig. 4. Modulation for one leg.

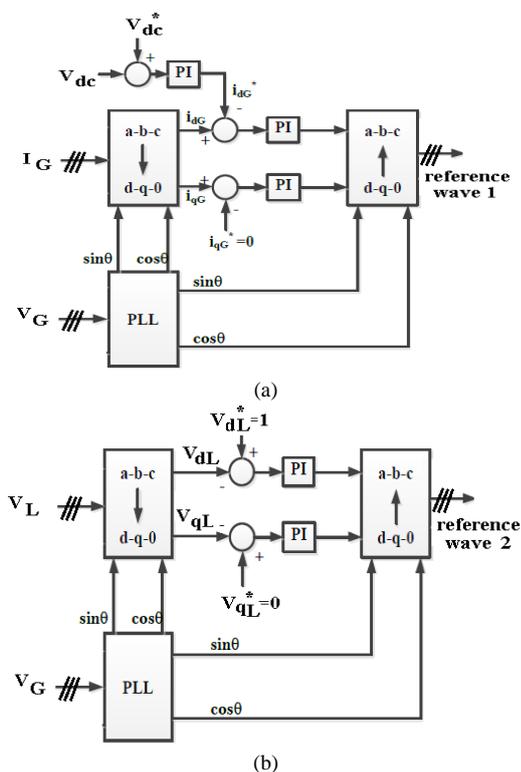


Fig. 5. Control block diagram of nine-switch inverter (a) inverter1 and (b) inverter2.

In stand-alone control mode, the output voltages need to be controlled in terms of amplitude and frequency and thus the reactive and, respectively, active power flow is controlled. In this state, voltage at inverter output (V_L) is measured and transformed to d - q frame using Park transformation. Resultant values are compared with reference values ($V_{dL}^*=1, V_{qL}^*=0$), the error signal is passed through PI controllers and transformed again to abc frame by Park inverse-transformation. Finally the reference voltage is applied to modulation unit to produce switching signals.

III. SIMULATION RESULTS

To show the performance of the proposed converter, some cases are simulated by using MATLAB/SIMULINK software. The main parameters used in simulation are listed in Table II.

Fig. 6 shows PV voltage and dc-link voltage. The dc/dc converter increases voltage of PV to reach suitable voltage of dc-link.

Fig. 7 shows power exchange of proposed system. When power of PV is bigger than power of load, the remain power is injected to grid. When power of PV is smaller than power of load, the required power is provided from grid. The power of PV is changed by changing of Irradiance.

The voltage and current of load are shown in Fig. 8.

TABLE II: SIMULATION PARAMETERS

Parameter	Value
PV system	25 Kw in $G=1000 \text{ W/m}^2$ and $T=25^\circ$
dc/dc converter	$L_1=L_2=500 \mu\text{H}, C_1=C_2=100 \mu\text{f}, f_s=25 \text{ KHz}$
Inverter	DC link voltage:1200V, switching frequency 5 KHz
Grid	380 V, 50 Hz
Load	18 KW

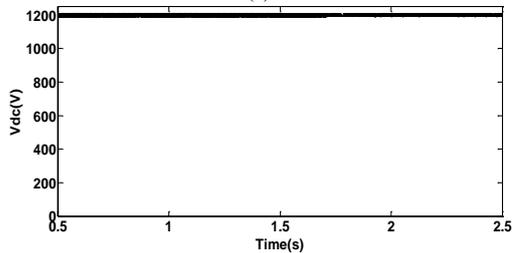
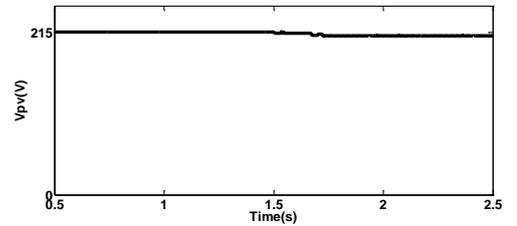
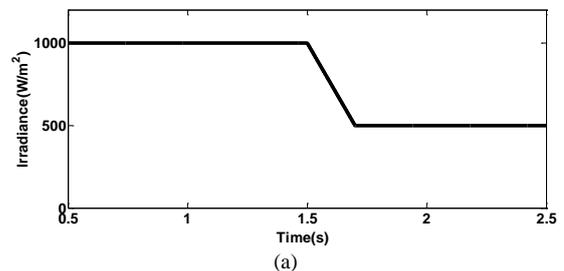


Fig. 6. (a) PV voltage and (b) dc-link voltage.



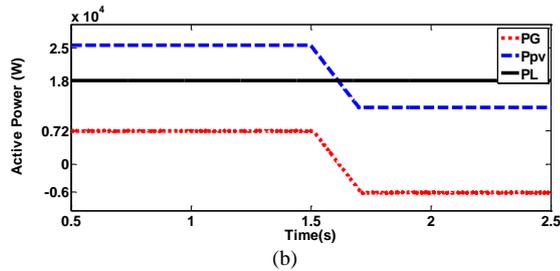


Fig. 7. Power exchange.

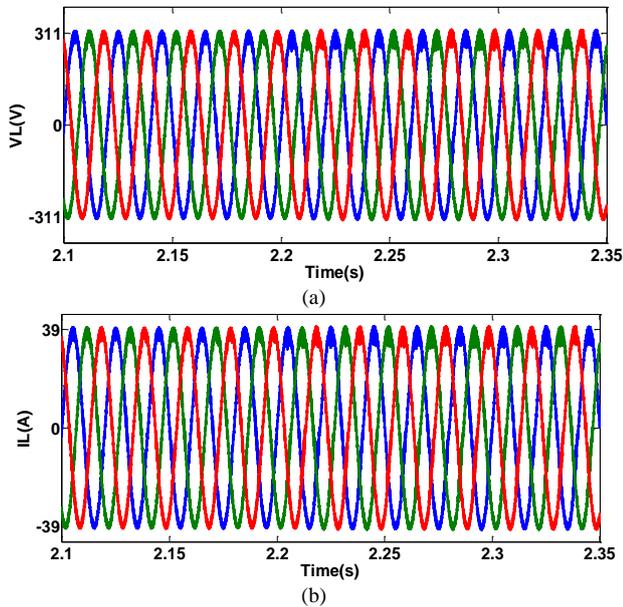


Fig. 8. Voltage and current of load (a) voltage and (b) current.

IV. CONCLUSION

In this paper, multi-stage converter based on combination of novel dc/dc converter and the nine-switch inverter is proposed. The nine-switch converter has two output ports. The proposed dc/dc converter has high gain. The presented systems connect photovoltaic energy sources to load and grid. In proposed structure photovoltaic source inject energy to load and grid. The presented structure dose not battery system to reserve energy. The simulation results show ability of presented systems in power generation.

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